

Mid-West University
Examinations Management Office
Final Examinations -2081

Level: Bachelors/CSIT /Semester: II

F. M: 60

Time: 3hrs.

P. M: 30

Subject: Micro-processor and Assembly Language (COM442)

Candidates are required to give their answer in their own words as far as practicable. The figures in the margin indicate full marks.

Group A

Very short Answer Questions

8 x 2 =16

1. What is reprogrammable system?
2. Write two difference of Von- Neumann and Harvard architecture.
3. Draw a timing diagram of fetch cycle.
4. What is interrupt descriptor table?
5. Define microprocessor?
6. What is SRAM?
7. What is DMA?
8. Define Assembler.

Group B

Short Answer Questions (Attempt any five)

5 x 4 =20

1. Explain the timing diagram of opcode fetch machine cycle.
2. What is system bus. Explain the types of system bus and block diagram.

3. Write short note
 - a) Polled Interrupt
 - b) Daisy Chain.
4. Write the assembly program to add two numbers (decimal) 38 and 64 then subtract decimal number 3 from the sum. The final result is to be stored in memory location 2100H.
5. Explain the operating mode of 8255 programmable peripheral interface.
6. Explain the Von-Neumann Architecture with block diagram.

Group C

Long Answer Question (Attempt any three)

1 x 8 = 8

1. Explain the addressing mode of 8085 microprocessor and its types.
2. Explain the 8259-interrupt controller with block diagram.
3. Explain the 8279 Programmable keyboard with block diagram.
4. Define assembling linking. Explain the steps of assembling linking and executing.

The End